

POWER SEMICONDUCTOR PACKAGE WITH STRAP

5

Sean T. Crowley
William M. Anderson
Bradley D. Boland
Eelco Bergman

ABSTRACT

A semiconductor package and a method for fabricating a semiconductor package
10 are disclosed. In one embodiment, the semiconductor package includes an exposed portion of a conductive strap at a package horizontal first surface and exposed surfaces of multiple leads at a package horizontal second surface. A power semiconductor die is mounted on a die pad connected to at least one lead having an exposed surface. Heat generated by the die within the package may be dissipated through thermal paths
15 including the exposed surfaces.

PROVISIONAL PATENT